



Tempus

Development of Embedded System Courses with implementation of Innovative Virtual approaches for integration of Research, Education and Production in UA, GE, AM

DesIRE



TRAINING CERTIFICATE

This is to certify, that

Sahaida Pavlo

as a participant of the international project "DesIRE – Development of Embedded Systems Courses with implementation of Innovative Virtual approaches for integration of Research, Education and Production in AM, GE and UA" (544091-2013-BE-JPCR) on the Tempus program of European Commission has visited Thomas More University College, Technology Campus De Nayer, Sint-Katelijne-Waver, Belgium for training sessions in the period of June 07th to June 20th 2015.

During the training the participant was trained in the following topics:

- Formula Flowcode Buggy,
- C for Embedded Systems,
- Embedded Communication ,
- Embedded Software,
- Embedded Operating Systems,
- Multicore Programming,
- Digital Signal Processing,
- CAD/CAM/CAE for Embedded Systems,
- Research in Embedded Systems.

The participant also visited different departments of the Thomas More University College, visited a printed circuit board assembly company, took part in the Altium Academic Roadshow for academic users and local industry, and took part in the project meeting on June 19th and in the discussion on the future project implementation.

ing. Dirk Van Merode, M.Sc.
DesIRE Coordinator
Thomas More University College

dr. Joan De Boeck
Head of Department Technology and Design
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